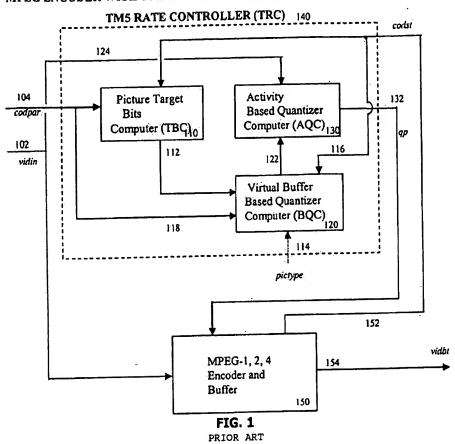
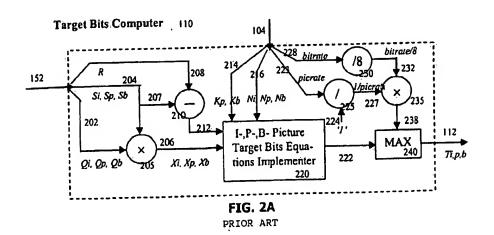
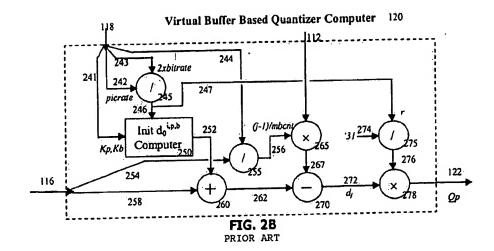
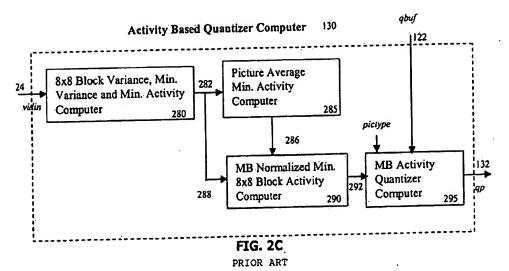
Inventor: Atul Puri et al.; Attorney Docket No. 13316/3295 REPLACEMENT SHEET 1 OF 28

# MPEG ENCODER WITH TM5 RATE CONTROLLER 100



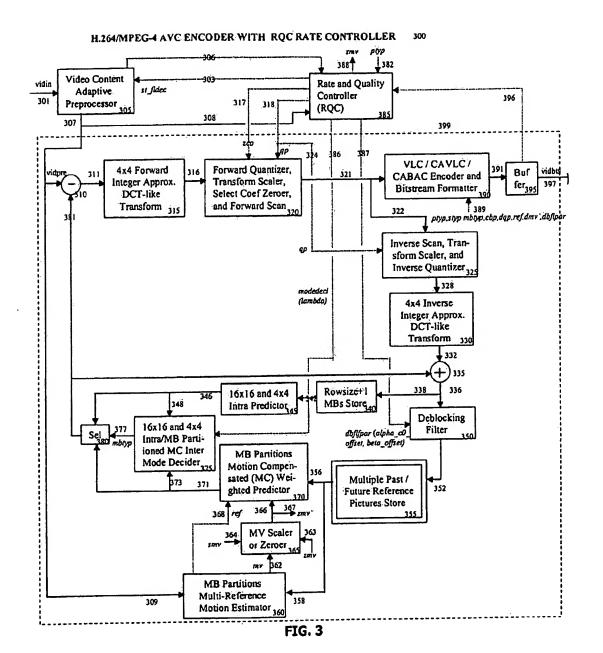


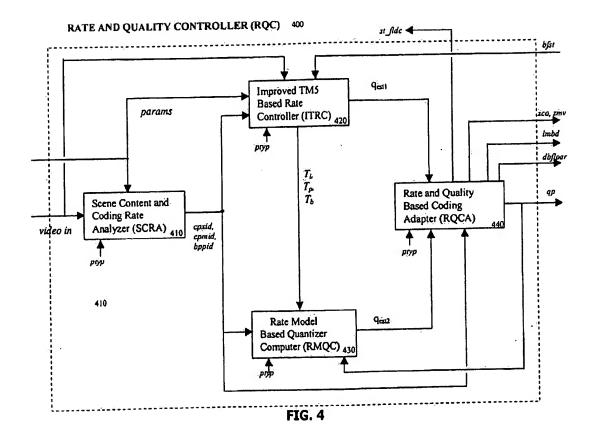


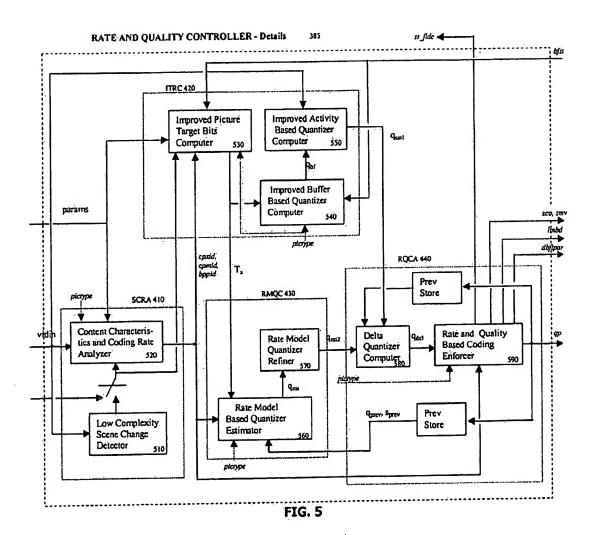


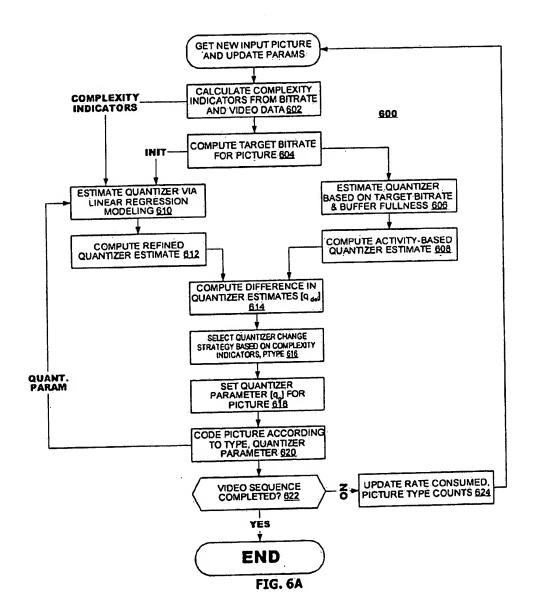
Inventor: Atul Puri et al.; Attorney Docket No. 13316/3295

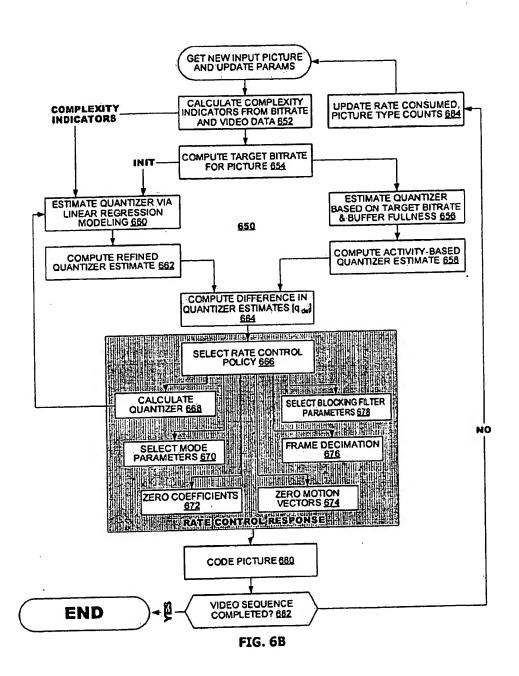
**REPLACEMENT SHEET 3 OF 28** 











## 700-Video frames coding order when employing 2 B-frame coding structure

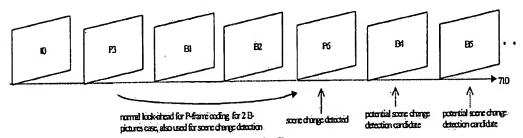


FIG. 7

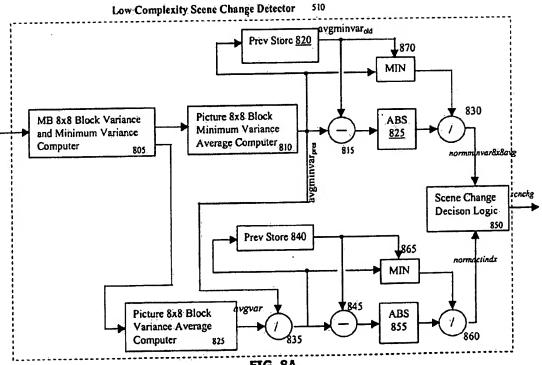
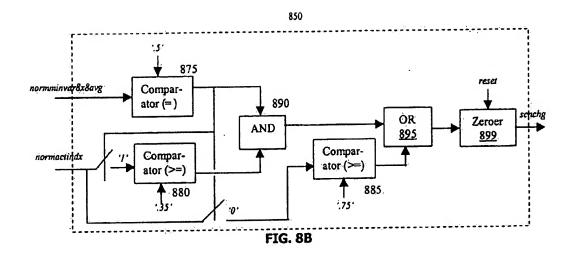


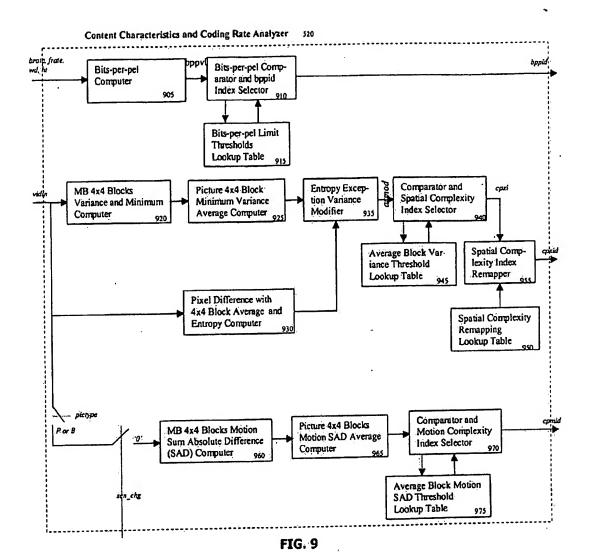
FIG. 8A

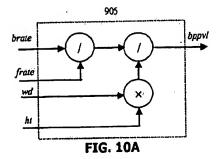
Application No. 10/811,983; Filed 03/30/2004 H.264/AVC CODER INCORPORATING RATE AND QUALITY CONTROLLER

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REPLACEMENT SHEET 11 OF 28

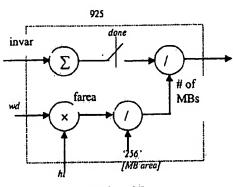


FIG. 10B

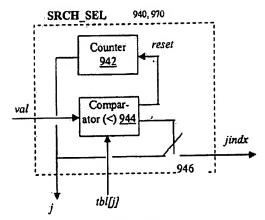


FIG. 10C

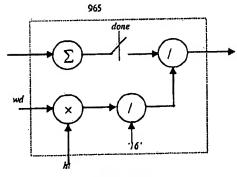
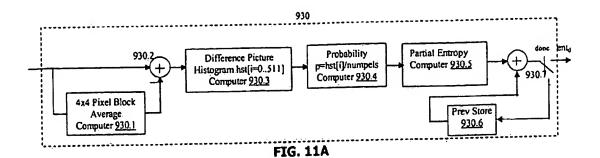
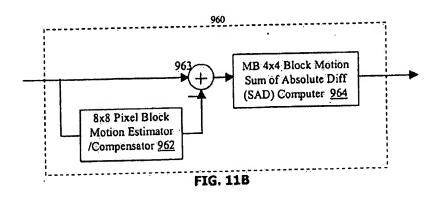
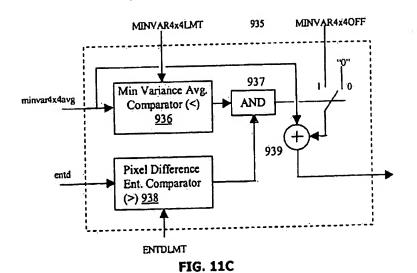


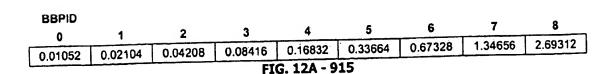
FIG. 10D

Inventor: Atul Puri et al.; Attorney Docket No. 13316/3295
REPLACEMENT SHEET 12 OF 28





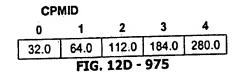




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CPID 0	1	2	3	4	5	6	7	8	9	10	11
1.0	2.0	4.5	8.5	12.5	17.0	22.0	28.0	34.0	41.0	50.0	60.0
			12	13	14	15	16	17			
			71.0	84.0	100.0	120.0	145.0	177.0			
				<del></del>	FIG. 12	R - 94	5				

CP	XID							
0	1	. 2	3	4	5	6	7	- 8
1	3	5	7	9	11	13	15	17
		.1	FIG	. 12C -	955			



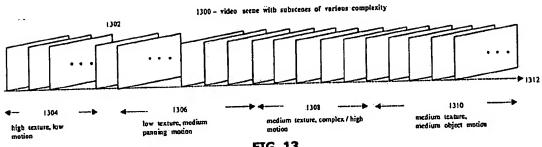
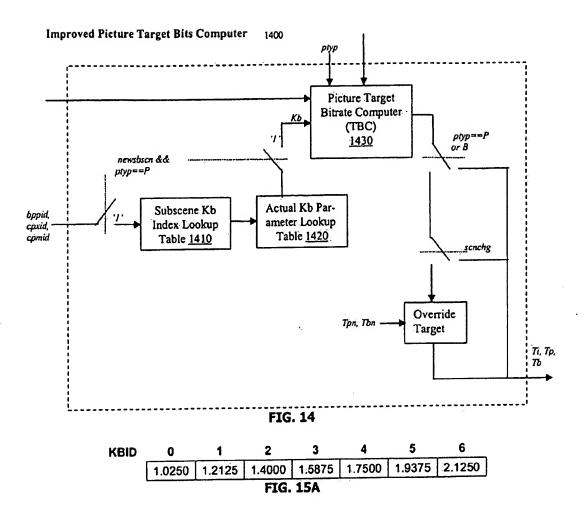


FIG. 13

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	_	CPXID						COMID	CPMID=1			CPXID									
CPMIC	)=2	0	1	2	3	4	5	6	7	8	CPMID	-1	0	1	2	3	4	5	6	7	8
	0	2	2	2	3	3	3	4	4	4		0	2	2	2	2	2	3	3	3	3
	1	2	2	2	2	3	3	3	4	4		1	2	2	2	2	2	2	3	3	3
	2	2	2	2	2	2	3	3	3	4		2	2	2	2	2	2	2	2	3	3
0	3	2	2	2	2	.2	2	3	3	3	٥	3	2	2	2	2	2	2	2	2	3
врріо	4	2	2	2	2	2	2	2	3	3	BPPID	4	2	2	2	2	2	2	2	2	2
80	5	2	2	2	2	2	2	2	2	3	8	5	1	2	2	2	2	2	2	2	2
	6	1	2	2	2	2	2	2	2	2		6	1	1	2	2	2	2	2	2	2
	7	1	1	2	2	2	2	2	2	2		7	1	1	1	.2	2	2	2	2	2
	8	1	1	1	2	2	2	2	2	2		8	1	1	1	1	2	2	2	2	2
			<u> </u>			<u> </u>				FIG	15B										

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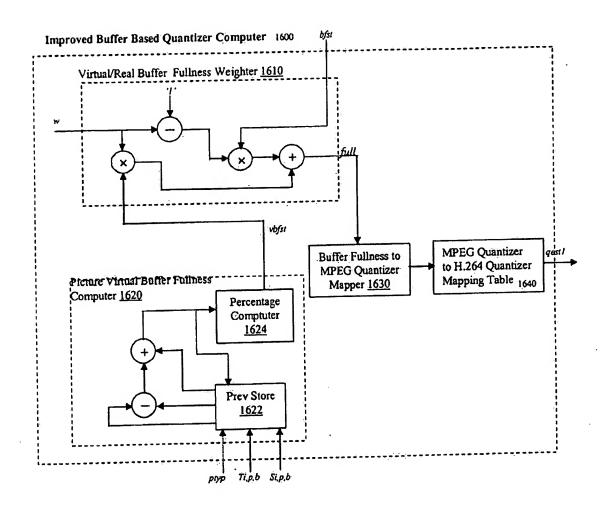


FIG. 16

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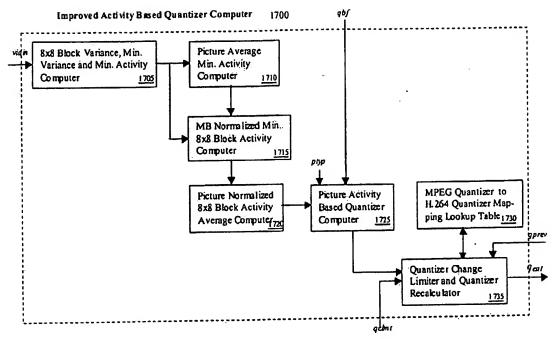


FIG. 17

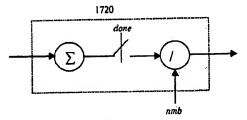


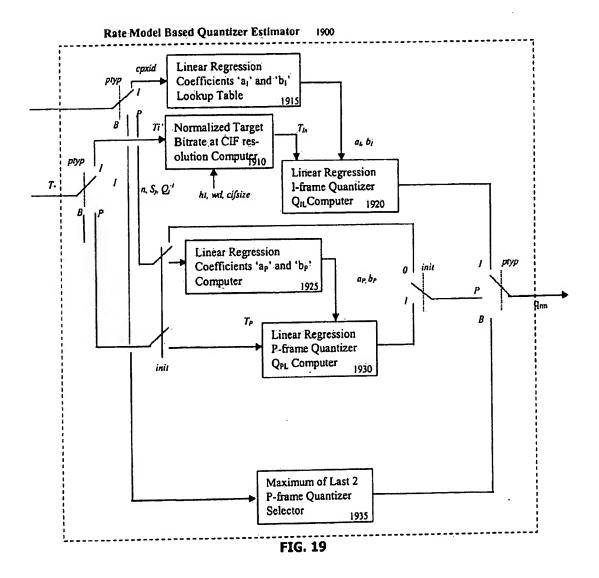
FIG. 18A

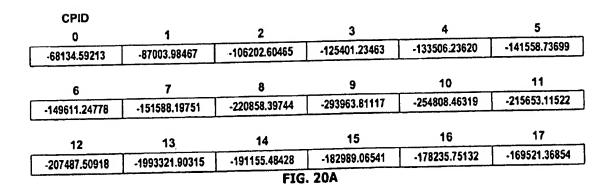
#### Application No. 10/811,983; Filed 03/30/2004 H.264/AVC CODER INCORPORATING RATE AND QUALITY CONTROLLER Inventor: Atul Puri et al.; Attorney Docket No. 13316/3295 REPLACEMENT SHEET 17 OF 28

qh:	264											
0	1	2	3	4	5	6	7	8	9	10	11	12
.250	.281	.315	.353	.396	.446	.500	.561	.623	.707	.794	.891	1.00
13	14	15	16	17	18	19	20	21	22	23	24	25
1.12	1.26	1.41	1.59.	1.78	2.00	2.25	2.52	2.82	3.18	3.56	4.00	4.49
26	27	28	29	30	31	32	33	34	35	36	37	38
5.04	5.65	6.35	7.13	8.00	8.98	10.08	11.31	12.70	14.25	16.00	17.96	20.16
39	40	41	4 <u>2</u>	43	44	45	46	47	48	· 49	50	51
22.63	25.39	28.51	32.00	35.92	40.31	45.25	50.80	57.02	64.00	71.83	80.64	90.51
	L	l	l	·		FIG. 1	8 <b>B</b>					

1800 qdiflmt  $\mathtt{nlmqnt}(qoff+f)$ qoff Prev Store Limiter ABS Comparator [0,30] SRCH Mq 1810 (>) 1812 1824 SEL 1802 / 1804 qprev – qoff <u>+-qdifim</u>i qindx:+qoff qindx+qoff - qprev qindx 1816 SIGN -1822 qprev - qoff FIG. 18C

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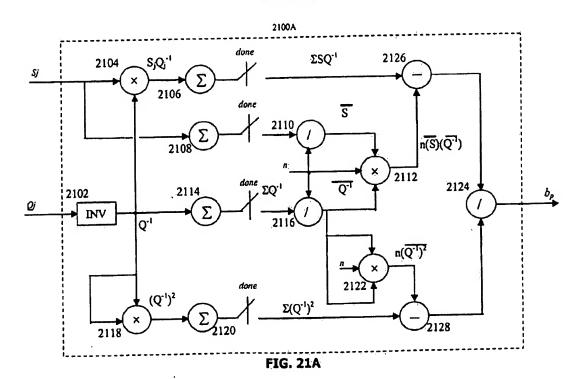


## Application No. 10/811,983; Filed 03/30/2004 H.264/AVC CODER INCORPORATING RATE AND QUALITY CONTROLLER

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CPID				<u> </u>	5
0	1	2	3	4	
3313453.21342	3993567.19336	4565785.16255	5138003.13174	5715464.15501	6104194.66665
6	7	8	9	10	11
6492925.17829	6678722.15535	9084900.80067	11517856.77655	10611605.70466	9705354.63278
12	13	14	15	16	17
9777071.15962	9848787.68646	9920504.21330	9992220.74014	10623397.89991	11435042.39299

FIG. 20B



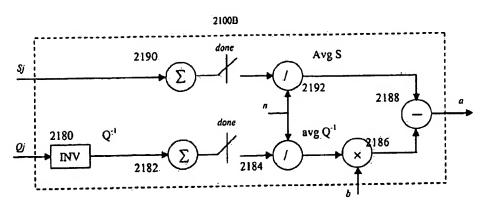
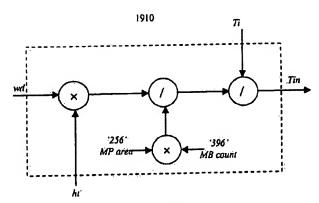
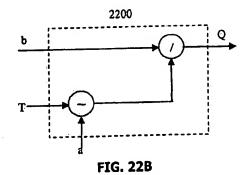


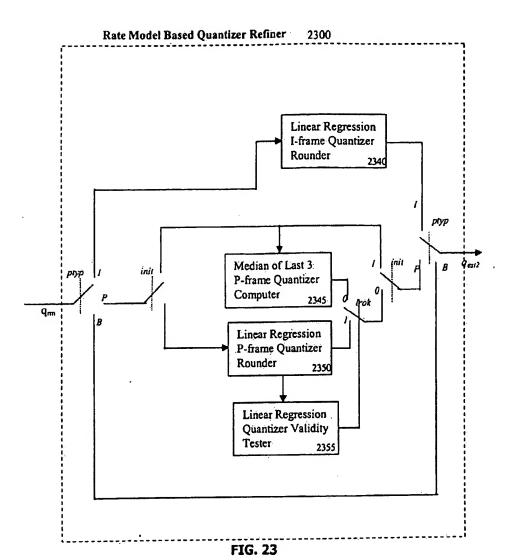
FIG. 21B

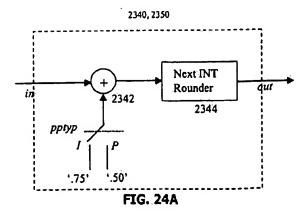


**FIG. 22A** 



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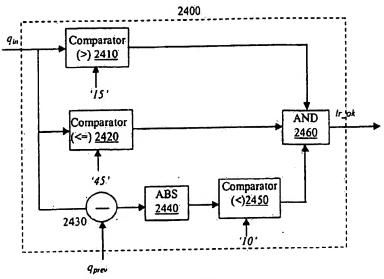


FIG. 24B

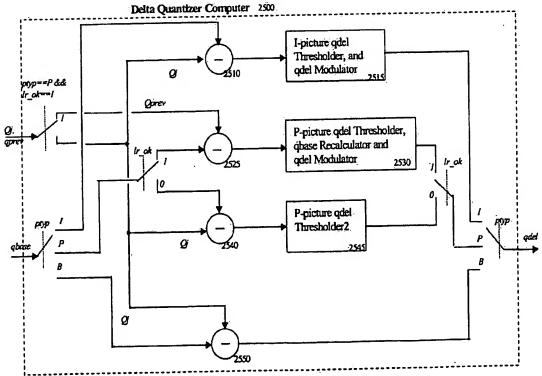
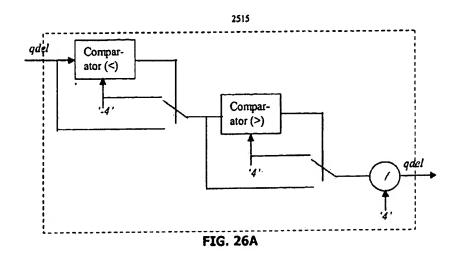
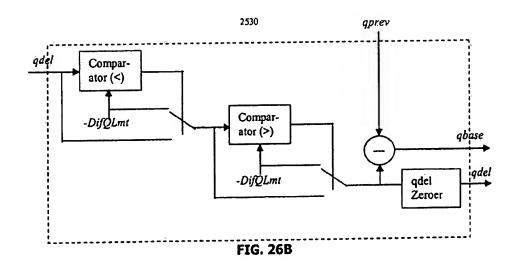
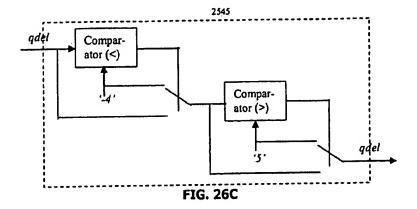


FIG. 25

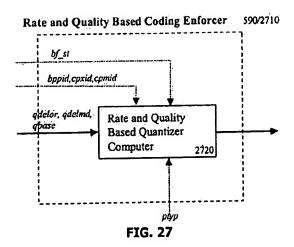
AND QUALITY CONTROLLER
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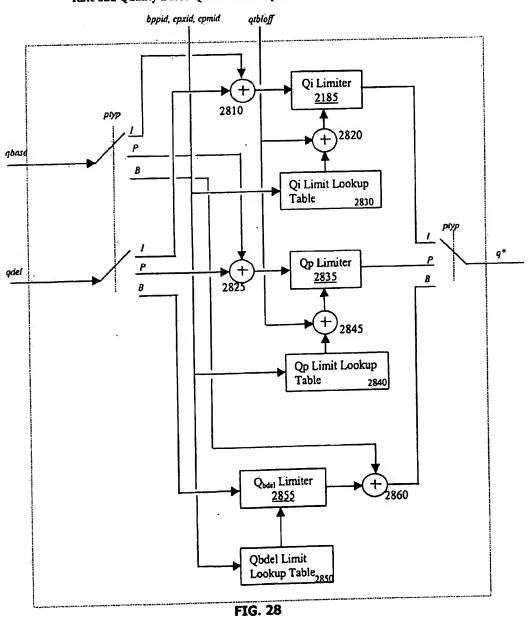


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H.264/AVC CODER INCORPORATING RATE
AND QUALITY CONTROLLER
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# Rate and Quality Based Quantizer Computer 2800



## Application No. 10/811,983; Filed 03/30/2004 H.264/AVC CODER INCORPORATING RATE AND QUALITY CONTROLLER

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		CPXID										
		0	1	2	3	4	5	6	7	8		
	0	10	11	12	13	14.	15	16	17	18		
	1	9	10	11	12	13	14	15	16	17		
	2	8	9	10	11	12	13	14	15	16		
^	3	7	8	9	10	-11	12	13	14	15		
BPPID	4	6	7	8	9	10	1.1	12	13	14		
8	5	5	6	7	8	9	10	11	12	13		
	6	4	5	6	7	8	9	10	11	12		
	7	3	4	5	6	7	8	9	10	11		
	8	2	3	4	5	6	7	8	9	10		

FIG. 29A

						CPXID				
		.0	1:	2	3	4	5	6	7	88
	0	10	11	12	13	14	15	16	17	18
	1	9	10	11	12	13	14	15	16	17
	2	8	9	10	11	12	13	14	15	16
_	3	7	8	9	10	11	12	13	14	15
BPPID	4	6	7	8	9	10	11	12	13	14
8	5	5	6	7	8	9	10	11	12	13
	6	. 4	5	6	7	8	9	10	11	12
	7	3	4	5	6	7	8	9	10	11
	8	2	3	4	5	6	7	8	9	10

FIG. 29B

						CPXID				
		Ó	1	2	3	4	5	6	7	8
	0	2	2	2	3	3	.3	4	4	4
	1	1.	2	2	2	3	3	3	4	4
	2	1	1	2	2	2	3.	3	3	4
_	3	1	1	1	2	2	2	3.	3	3
ВРР10	4	1	1	1	1	2	2	2	3	3
8	5	0	1	1	1	2	2	2	2	3
	6	0	0	1	1	1	2	2	2	2
	7	0	0	0	1	1	1	2	2	2
	8	0	0	0	0	1	1	1	2	2

FIG. 29C

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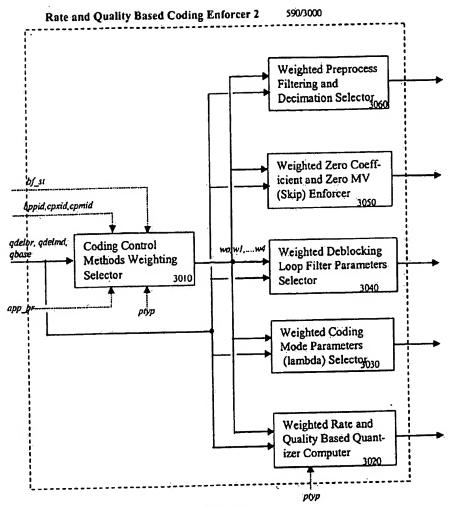


FIG. 30

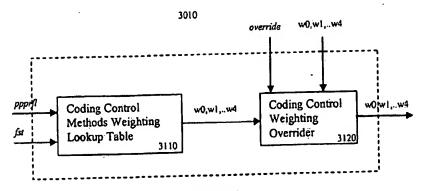


FIG. 31

AND QUALITY CONTROLLER
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					Ä	1						
	•				3	0.37	0.05	0.16	0.01	0.28	0.06	0.19
			2	2 [	0.09	0.12	0.46	0.26	0.21	0.15	0.07	0.36
			1 [	0.22	0.45	0.16	020	0.24	0.45	0.18	0.09	0.38
		0	0.09	0.09	0.05	0.27	021	027	0.31	0.25	0.09	0.33
app	_pr	ō	1	2	3	4	5	6	0.13	0.26	0.08	0.39
	0	023	0.30	0.18	0.26	0.07	0.07	0.26	0.14	0.23	0.1	0.3
	1	0.11	0.48	0.25	0.03	0.23	0.15	0.19	.0.12	0.27	0.08	0.34
	2	0.08	0.21	0.45	0.18	0.15	0.12	0.15	0.14	0.21	0.08	0.35
	3	0.44	0.39	0.07	0.09	0.31	0.45	028	0.11	0.23	0.09	0.29
<b>*</b>	4	0.47	0.30	0.28	0.12	0.08	0.35	0.10	0.12	0.24	0.07	0.32
ğ	5	0.44	0.37	0.10	0.30	0.29	0.15	0.41	0.13	0.2	0.08	
	6	0.20	0.47	0.38	0.22	0.44	027	0.27	0.1	0.22		
	7	0.10	0.12	0.47	0.27	0.10	0.09	022	0.11			
	8	0.30	0.22	0.49	0.46	0.18	0.49	0.47				
	9	0.11	0.44	0.07	0.03	0.36	0.09	0.35				

FIG. 32

Weighted Rate and Quality Based Quantizer Computer 3020

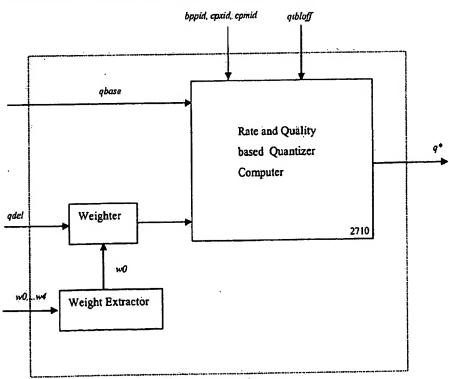


FIG. 33